

Herga Name	Description	Symbol
Double Operation (Also called 4PST)	Two groups of Double-Pole, Single-Throw circuits (Four-Pole, Single-Throw) Each Double-Pole, Single-Throw circuit acts as one SPST N/O and one SPST N/C circuit, staggered	
DPCO	Double-Pole, Change-Over	
DPST + DPST	Two Double-Pole, Single-Throw circuits, staggered. Each Double-Pole, Single-Throw circuit acts as one SPST N/O and one SPST N/C circuit, staggered	
DPST N/O	Double-Pole, Single-Throw: Normally Open	
Open collector	Transistor output: read IFU for implementation instructions	

Herga Name	Description	Symbol
Single Operation	Special case of Double-Pole, Single-Throw Acts as one SPST N/O and one SPST N/C circuit, staggered	
SPCO + SPCO	Two Single-Pole, Change-Over circuits, staggered	
SPDT (Also called SPCO)	Single-Pole, Double-Throw (or Single-Pole Change-Over)	
SPST N/C	Single-Pole, Single-Throw: Normally Closed	
SPST N/O	Single-Pole, Single-Throw: Normally Open	